

DOCKET NO: 241190US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :

TAKESHI YOSHIDA

: EXAMINER: CHANG, D.

SERIAL NO: 10/632,866

:

FILED: AUGUST 4, 2003

: GROUP ART UNIT: 2819

FOR: SEMICONDUCTOR INTEGRATED :
CIRCUIT HAVING SYSTEM BUS
DIVIDED IN STAGES

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated December 10, 2004, please amend the above-identified application as follows: ,

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.

04/06/2005 JADD01 00000096 10632866
01 FC:1201 200.00 OP